

07/09/03  
17611 U.S. PTO

PATENT APPLICATION  
Docket No. 4591-332  
Client Ref. No. ID12019-US

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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I HEREBY CERTIFY THAT THIS PAPER AND ENCLOSURES AND/OR FEE ARE BEING DEPOSITED WITH THE UNITED STATES POSTAL SERVICE "EXPRESS MAIL POST OFFICE TO ADDRESSEE" SERVICE UNDER 37 CFR 1.10 ON THE DATE INDICATED ABOVE AND IS ADDRESSED TO: MAIL STOP PATENT APPLICATION, COMMISSIONER FOR PATENTS, P.O. BOX 1450, ALEXANDRIA, VA 22313-1450.

JILL JEANS  
(SENDER'S PRINTED NAME)

*Jill Jeans*  
(SIGNATURE)

21908 U.S. PTO  
10/618206  
07/09/03

Mail Stop Patent Application  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Enclosed for filing is a patent application under 37 CFR 1.53(b) of:

Inventor: Cheol-Ung Jang and Young-Joon Choi  
For: MULTIPLE CHIP SYSTEM INCLUDING A PLURALITY OF NON-VOLATILE SEMICONDUCTOR MEMORY DEVICES

Applicant requests FIG. 3 to be published with the application.

Enclosures:

- ☒ Specification (pages 1- 4); claims (pages 5-6); abstract (page 7)  
☒ FIVE sheet(s) of FORMAL drawings (comprising 5 figures)  
☒ Declaration or Combined Declaration and Power of Attorney (unsigned)  
☐ Assignment with cover sheet  
Assignee Name and Address: Samsung Electronics Co., Ltd.  
Suwon-city, Kyunggi-do, Republic of Korea  
☒ Certified copy of Priority Document No. 10-2002-0039632, filed July 9, 2002

CLAIMS AS FILED				
For	Number Filed	Number Extra	Rate	Basic Fee \$750
Total Claims	10-20	0	x \$ 18 =	0
Independent Claims	2-3	0	x \$ 84 =	0
TOTAL FILING FEE				\$750



20575

PATENT TRADEMARK OFFICE

Respectfully submitted,  
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